**Programming Tools - Compilation and linking Guided Assignments**

Typographical conventions

We use the following conventions in this guide:

gcc The name of a specific command or file

filename You should replace file with a specific name

WARNING Output that you see on the screen

Getting started with gcc

1. Log into the Linux server using your respective ids

2. Copy the C source file simple\_program.c. Compile the file with the following command:

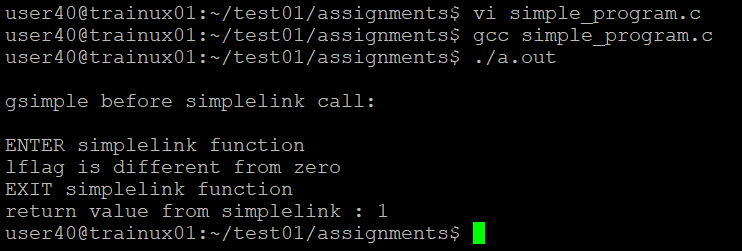
gcc sourcefilename.c

Where sourcefilename.c is the name of the C source code

3. Observe the executable formed is called a.out. Execute the file using the command:

./a.out

(Here ./ implies that the path of a.out is the current directory)



4. Once again compile the source code with the following command:

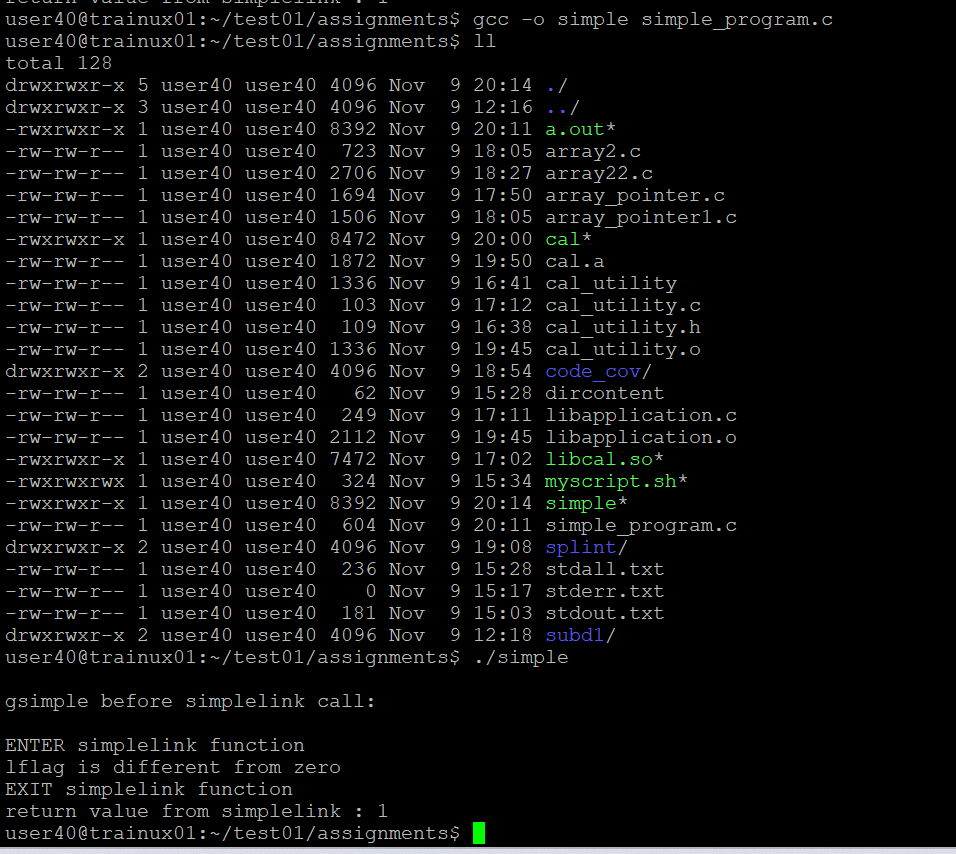
gcc –o outputfilename sourcefilename.c

Where outputfilename is the name of the executable file. After this command gets executed successfully, the name of the executable is not a.out but whatever is given as outputfilename

Execute the output file using the command:

./outputfilename

Object files



5. Once again compile the source code with the following command:

gcc –c sourcefilename.c

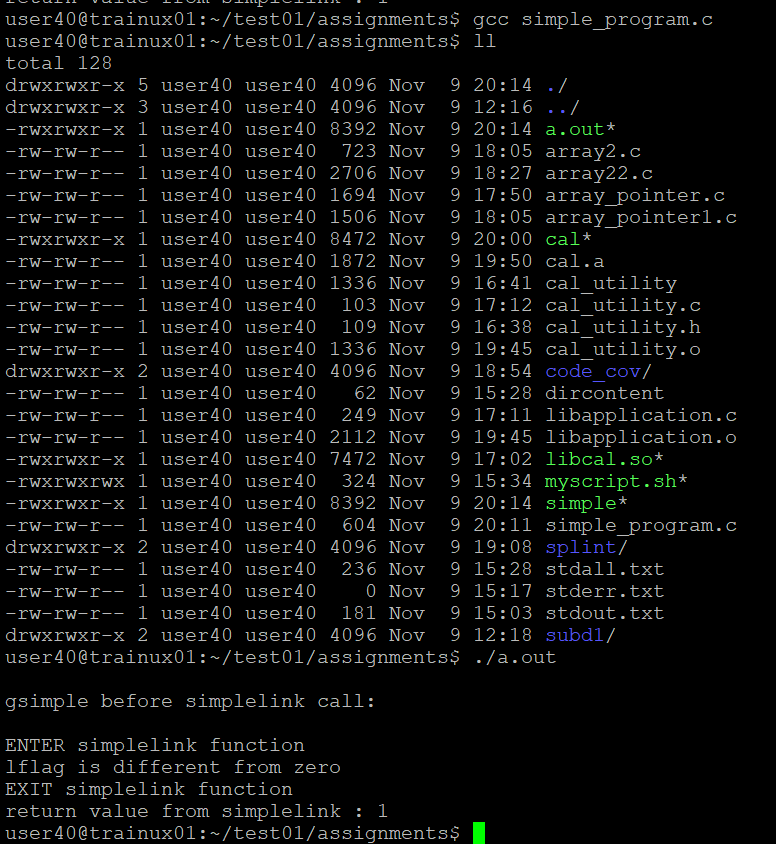
Observe using ls that an object file called sourcefilename.o is created in the directory

What is the difference between an executable and an object file?

Now, create the executable from the object file using the following command:

gcc sourcefilename.o

Additional gcc switches



6. Copy the program program\_warn.c as directed by the facilitator. Compile the program using gcc without any switch. Observe the result of compilation

7. Now, compile the program with the –Wall switch. Observe the warnings that are now given by the compiler.

8. Now, compile the program with the –ansi switch. Observe the warnings that are now given by the compiler.

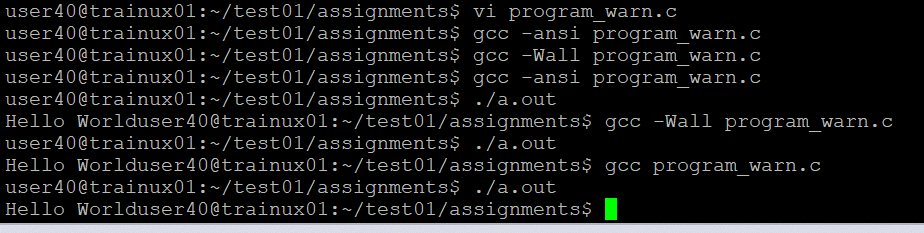
A screen shot of a computer code

Description automatically generated

By compiling with gcc without any switch ,compiling the program with the

–Wall switch,by compile the program with the –ansi switch there are no errors

Got the output as Hello world in all the three cases.



Linking

9. Write a program linking1.c, which has main function. Inside main there should be a call to another function which is defined in another file linking2.c

10. Compile the file linking1.c using the following command:

gcc –c –Wall linking1.c

Observe the warnings received

Again compile the file linking1.c using the following command:

gcc –Wall linking1.c

Observe the errors received

A computer screen with white text

Description automatically generated

11. Now create a header file prototype.h, which contains the prototype of the function defined in linking2.c. Let the file content begin and end with lines as below, to avoid multiple inclusion of .h file.

#ifndef \_\_PROTOTYPE\_H\_

#define \_\_PROTOTYPE\_H\_

void display(); // an example declaration of function defined in prototype.h

#endif //end of \_\_PROTOTYPE\_H\_

12. Include this file in linking1.c using the following statement:

#include <prototype.h>

and again compile linking1.c using the following command:

gcc –I. –c –Wall linking1.c

The –I switch tells gcc where the included header files have to be searched. Thus –I. implies that the current directory needs to be searched by gcc

Compare the result with the earlier compilation using the same command

13. Similar effect can be achieved by including the file prototype.h using the following

command

#include “prototype.h”

and then compiling using the following command

gcc –c –Wall linking1.c

If the included file name is given in double quotes, searching for the file typically starts where the source program was found. If it is not found there, then searching follows the paths given by the –I flag and implementation defined paths.

If the included file name is given in <>, searching follows the paths given by the –I flag and implementation defined paths.

14. Now after all the compilation warnings have been removed, link the 2 files together:

gcc –c –Wall linking1.c

gcc –c –Wall linking2.c

gcc linking1.o linking2.o

Linking1.c

A computer screen with colorful text

Description automatically generated

15. Do not include the header file prototype.h containing the prototype of the function in the file linking1.c.

Instead of that include the file linking2.c in the file linking1.c using the following statement:

#include “linking2.c”

Now repeat the following three commands:

gcc –c –Wall linking1.c

gcc –c –Wall linking2.c

gcc linking1.o linking2.o

Observe the error received.

Thus we see that the inclusion of a C source file in another C source file is NOT a correct way of writing programs. Imagine a scenario where there are 100 source files each having function calls to functions defined in other files. It would be a chaos trying to include files in each other, with no guarantee that the files would compile successfully

Is it right to include C function definitions instead of their prototypes in a header file?

· From the above explanation, it follows that header files should only contain declarations (prototypes) of functions and not their definitions

Is it right to include C global variable definitions in a header file?

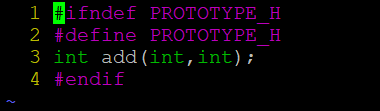
· If a global variable is defined in a header file, and that header file is included in multiple source files linked together, then it would give an error of multiple definitions for a single variable. Thus global variables should be defined in a C source file, while its extern declaration may be included in a header file. This header file may then be included in all the other source files accessing that global variable

Linking2.c

A computer code with colorful text

Description automatically generated

Prototype.h



16. Do not include the file linking2.c in linking1.c. Instead of that include the file prototype.h and proceed as mentioned in the points 15 and 16.

Optional Assignments on Preprocessor directive and Assembling

Preprocessor directives

1. Refer prepr7.c. Go through the program and find out what should be the result of these programs after preprocessing. Note that you only have to concentrate till the preprocessed stage.

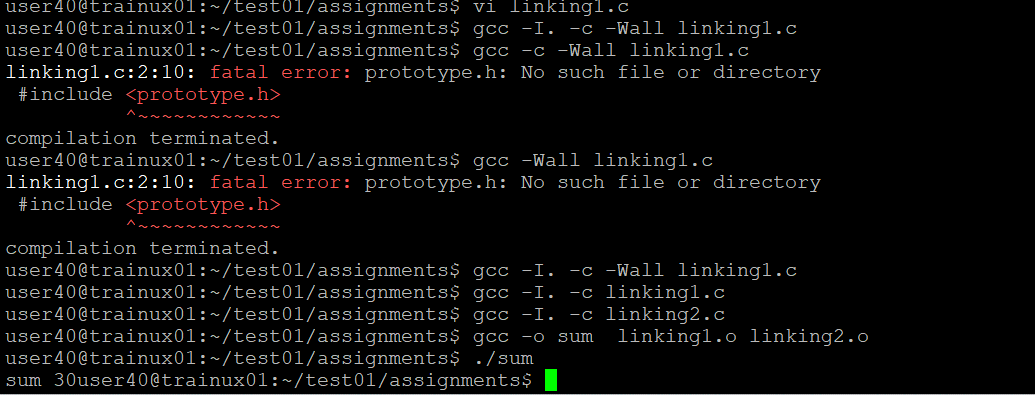
After thinking the answers, preprocess your program with the following command:

gcc –E sourcefilename.c

Compiler’s output

1. Copy the program simple\_program.c as directed by the facilitator. Compile the programs using gcc with the -S option such that only compilation takes place and assembling does not take place.

Observe that variables are represented as addresses and not as names in the resulting low-level code



The output is obtained as sum 30.